



Department of Electrical Engineering
Indian Institute of Technology, Delhi
Hauz Khas, New Delhi 110016

Date: **November 21, 2024**

FT/2023/11/19- IA/2402

Sealed/online/e-mail quotations are invited for Design and development of Automotive test setup. The required specifications are given below.

Design and development of Automotive test setup

1. Space optimized design on single PCB of 20 x 20 cm
2. PCB should include the custom SPAD detector, bias voltages required for the detector, FPGA processing capability of 664 x 64 SPAD array, USB interface
3. The clock frequency must be atleast 200 MHz
4. The power consumption must be less than 1 W in total.
5. The Processing logic must be capable of handling 10-bit raw image at higher than 30 fps.
6. The interface connection must cater to digital video, power, control and diagnostics
7. Diagnostic and Built-in self test for ASIL B applications
8. The complete design files and BOM Analysis

Dr. M. Sarkar
(Chairman, purchase committee)

Terms and Conditions

1. The quote should reach the following address on or before **2.12.2024, 5 PM**.
Dr. M. Sarkar
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Block II, Room 333
IIT Delhi, Hauz Khas
New Delhi, 110016
msarkar@ee.iitd.ac.in
2. Please quote prices for FOB New Delhi, inclusive of all taxes and duties.
3. Quote should be in Indian Rupees for agents of Indian manufacturers, or in foreign currency, for agents of foreign manufacturers, and needs to be valid for at least three months.
4. Please specify all of your terms and conditions clearly, including delivery period.
5. Mode of payment for purchases in foreign currency are through irrevocable letter of credit, or through wire transfer on delivery. Only bank charges within India are payable by IIT Delhi, all bank charges outside India are the responsibility of the seller. For purchases in INR, payment is on delivery.
6. The Institute reserves the right to accept or reject any or all quotations without assigning any reasons thereof.

Dr. M. Sarkar
(Chairman, purchase committee)